

## MINIATURE FIBER OPTIC MEMS SWITCH

## OVERVIEW

The $s x$ series are miniature opto-mechanical switches for fiber optic communication systems and submodules. The switch is available in latching variants, with $1 \times 1,2 \times 1$, $2 \times 2$, The switch offers smallest size, ease of integration and the established reliability of Sercalo's MEMS components.
The package is one of the smallest in the industry. It is optimized for low cost production while maintaining high reliability. The component meets Telcordia 1221 quality standards.

## FEAT-URES

- $23 \times 10 \times 6 \mathrm{~mm}$ size
- TTL or CMOS logic
- latching
- $2 \times 2,2 \times 1,1 \times 1$ variants
- single or multimode fiber


## APPLICATIONS

-Protection Switching
-Reconfiguration

- Optical Subsystems
-Array integration


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## DESCRIPTION

In the sx switches the optical switching function is realised by a silicon MEMS chip, on which a mirror can be moved in and out of the optical path by electrostatic actuation. The miniature SX switch is available as latching variant where a bistable suspension mechanism keeps the last selected state in power off. The non-latching type (i.e. SXNA) is not recommended for new designs.
To operate the switch 5 V and 0 V are applied on pins 1 and 2, which are used by the internal DC-DC converter to supply a high voltage for the actuator control. CMOS or TTL logic levels on pins 3-4 control the electrostatic actuator. To set the switch state pin 3 respectively pin 4 are set to logic high ( 5 V ) for 20 ms and the corresponding switch state is selected. At rest pins 3 and 4 should be pulled to 0 V and must not be floating.



Figure 1: Insertion loss distribution


Figure 2: spectral response over temperature


SIDE VIEW


3 CR select, $5 \mathrm{~V}, 10 \mathrm{~ms}$ pulse sets cross state
4 BR select, $5 \mathrm{~V}, 10 \mathrm{~ms}$ pulse sets bar state
Figure 4: Pin layout SXLA2x1 latching


Figure 5: Electrical Schematic Diagram

