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FRD13000 FULLY SCREENED 6A, 9KV REED RELAY



A complete RF screen around the coil assembly for a low loss transmission path, results in better carry current at elevated temperatures for this open frame series. An anti-corona sleeve is fitted around the reed switch for improved RF voltage isolation.

This type is widely used in HF Antenna Tuning Unit (A TU) applications.

Available as Form A (SPNO) or Form B (SPNC) contact configuraions

Features

- New Fully Screened Coil for Low RF Loss
- Up to 9kVDC and 6A at 30MHz
- Ideal for HF Antenna Tuning Applications
- Customizing Facility



SPECIFICATIONS

Contact	Units	Condition	FRD13506	FRD13503	FRD13504
Action (Form A, B or Latching)			А	А	В
Switching Voltage	V	DC max	20	20	20
Switching Current	А	DC max	1	1	1
Carry Current	А	RMS at 30MHz max	6	6	6
Isolation	kV	DC max	9	9	9
Isolation	kV	RF peak (F=2MHz)	8	8	8
Capacitance	рF	coil/screen gnd	0.5	0.5	0.5
Lifetime	operations	dry switching	10 ⁹	10 ⁹	10 ⁹
Contact Resistance	mOhms	maximum (typical)	50 (15)	50 (15)	50 (15)
Insulation Resistance	Ohms	minimum (typical)	10 ¹⁰ (10 ¹³)	10 ¹⁰ (10 ¹³)	10 ¹⁰ (10 ¹³)
ESR at 4.5A, 30MHz	mOhms	typical	30	30	30
Coil at 20°C					
Nominal Working Voltage	VDC		12	24	24
Must Operate	VDC	max	9	15	16
Must Release	VDC	min	2	3	3
Nominal Resistance	Ohms	+/-10%	98	900	900
RF Screening			Full	Full	Full
RF Screening Connection		pin position	2 & 5	2 & 5	2 & 5
Coil Connections		pin position	1&6	1 & 6	1(+) & 6

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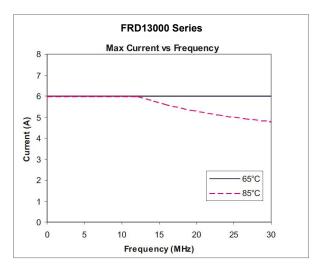
Relay	Units	Condition	FRD13506	FRD13503	FRD13504
Operate Time (Incl. Bounce)	ms		3	3	1
Release Time	ms		1	1	3
Isolation Contact to all other Terminals	kV	DC max	9	9	9
Isolation Coil to Screen	kV	DC max	0.5	0.5	0.5
Capacitance Contact to all other Terminals	pF	contacts open	1.5	1.5	1.5
Environmental Conditions					
Storage Temperature Range	С°			-55 to +125	
Operating Temperature Range	С°	Limited current*		-40 to +85	
Shock	g	11ms 1/2 sine pk		100	
Bump	g	6ms 1/2 sine pk		40	
Vibration	g	10- 500Hz		10	
Weight	gm		20	20	23

*see graphical data overleaf.

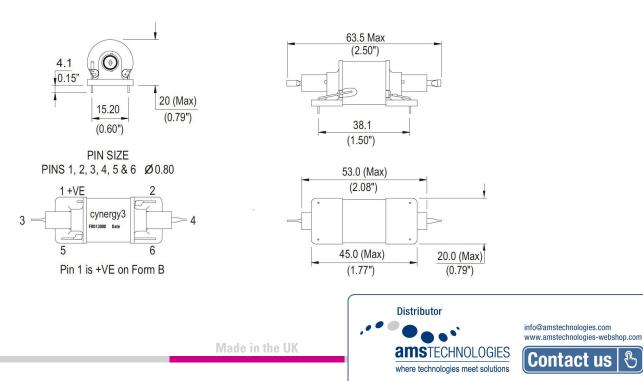
Please refer to this document for circuit design notes: <u>https://www.cynergy3.com/blog/reed-relay-application-notes</u>







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