SPECIFICATIONS

Modulation Input

Input Impedance
Digital Input (SMB Male)

RF Output

Center Frequency (Fc)

Output Power (SMA Female)

Rise/Fall Time

RF Contrast Ratio

Harmonic Distortion

Output Impedance

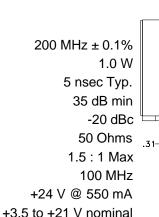
Output VSWR

Modulation Bandwidth

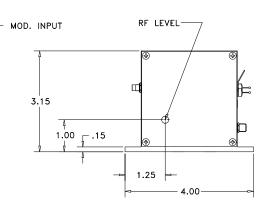
Power Supply Voltage (+Vcc)

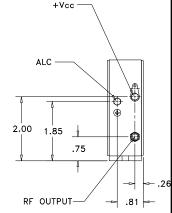
ALC Voltage Level

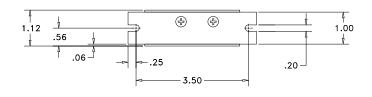
50 Ohms Standard TTL Levels



2.00







OUTLINE DRAWING

Notes:

- 1. Output power factory set to 1 W at a 2.4 V, 30 mA input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
- 2. When calculating the contrast ratio, it is understood that only the power of the 200 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.
- 3. A +21 Volt nominal input on the ALC corresponds to full RF output power. Zero RF power occurs at an ALC voltage below +3.5 Volts. Full RF power occurs if ALC input is left unconnected.



Document

10/16/13

Control

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TOLERANCES: .XX ±.01 .XXX ±.005	DR	Geri Scholz 10/14/2013				
MATERIAL: ROHS	CHK		AODR 1200AF-DIF0-1.0			
FINISH: Compliant	APP APP		PART NUMBER: 97-03307-	-26	REV:	1 of 1