

Preliminary Specifications

DESIGN GOALS

Modulation Input

Input Impedance
Digital Input (SMB Male)

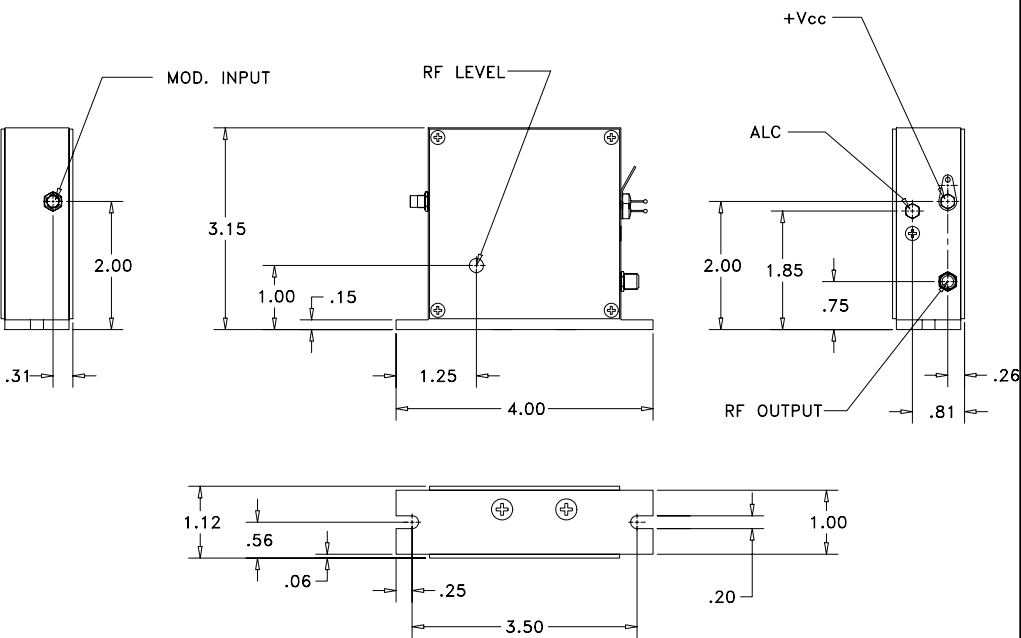
50 Ohms
Standard TTL Levels

RF Output

Center Frequency (Fc)
Output Power (SMA Female)
Rise/Fall Time
RF Contrast Ratio
Harmonic Distortion
Output Impedance
Output VSWR
Bandwidth
Power Supply Voltage (+Vcc)
ALC Voltage Level

40 MHz \pm 0.1%
0.5 W
25 nsec Typ.
35 dB min
-20 dBc
50 Ohms
1.5 : 1 Max
20 MHz
+24 V @ 550 mA
+3.5 to +21 V nominal

OUTLINE DRAWING



Notes:

1. Output power factory set to 0.5 W at a 2.4 V, 30 mA input. Power stability less than 5% over the heat sink's ambient temperature range of 0-40° C, after 5 minute warm-up.
2. When calculating the contrast ratio, it is understood that only the power of the 40 MHz fundamental shall be used. The higher harmonics have no effect on the AO modulator's performance.
3. A +21 Volt nominal input on the ALC corresponds to full RF output power. Zero RF power occurs at an ALC voltage below +3.5 Volts. Full RF power occurs if ALC input is left unconnected.



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TOLERANCES: .XX \pm .01 .XXX \pm .005	DR	T. Moon 10/23/2014	Gooch & Housego		
MATERIAL: RoHS	CHK		DESCRIPTION: AODR 1040AF-DIF0-0.5		
FINIS Compliant	APP				
	APP		PART NUMBER: 97-03307-75	REV: 1	1 of 1